



Substitute Form PTO-1449

U.S. Department of Commerce
Patent and Trademark Office

Attorney's Docket No.

06457-007003

Application No.

10/036,431

Information Disclosure Statement
by Applicant

(Use several sheets if necessary)

Applicant

Donald V. Smart

Filing Date

January 7, 2002

Group Art Unit

1725

(37 CFR 1.98(b))

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
SMH	AA	5,208,437	05/1993	MIYAUCHI et al.			
SMH	AB	5,021,362	06/1991	CHLIPALA			
SMH	AC	5,066,291	11/1991	STEWART			
SMH	AD	5,293,025	03/1994	WANG			
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SMH	AF	5,685,995	11/1997	SUN et al.			
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SMH	AL	5,569,398	10/1996	SUN			
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SMH	AN	5,128,601	07/1992	Orbach et al.			
SMH	AO	4,847,850	07/1989	Kafka et al.			
SMH	AP	5,163,062	11/1992	Moberg			
SMH	AQ	5,521,932	05/1996	Marshall			
SMH	AR	5,696,778	12/1997	MacPherson			
SMH	AS	5,998,759	12/1999	Smart			
SMH	AT	6,337,462	01/2002	Smart			
SMH	AU	Re 34,192	03/1993	Bear			
SMH	AV	5,453,594	09/26/1995	KONECNY			

Foreign Patent Documents or Published Foreign Patent Applications

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SMH	AW	5-104266	04/1993	JAPAN *				
SMH	AX	WO 94/06182	03/1994	PCT *				

Examiner Signature

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SMH	BA	WO 96/15870	05/1996	PCT *				
SMH	BB	JP62086851A2	4/1987	Japan	H01L 27	01	X (English-language abstract only)	

Other Documents (include Author, Title, Date, and Place of Publication)

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SMH	BB	Cook, P.W. et al.; "Connections and Disconnections on Integrated Circuits Using Nanosecond Laser Pulses"; Appl. Phys. Lett., Vol. 26, Number 3; pp. 124-126; February 1, 1975
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SMH	BL	Litwin et al.; "Laser Adjustment of Linear Monolithic Circuits"; Laser Institute of America, International Congress on Applications of Lasers and Electro-Optics; vol. 38, pp. 166-173; 1983
SMH	BM	Murray et al.; "Generation of 1.5- μ m Radiation Through Interactivity Solid-State Raman Shifting in Ba(NO ₃) ₂ Nonlinear Crystals"; Optics Letters; vol. 20, No. 9; pp. 1017-1019; 1995
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SMH	BP	Price et al.; "A passive Laser-Trimming Technique to Improve the Linearity of a 10-Bit D/A Converter"; IEEE Journal of Solid-State Circuits; Vol. SC-11, No. 6; pp. 784-794; December 1976
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Substitution (Modifications) TRADEMARKS	Form PTO-1449	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 06457-007003	Application No. 10/036,431
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))			Applicant Donald V. Smart	
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Other Documents (include Author, Title, Date, and Place of Publication)		
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SMH	BR	Scarfone, et al.; "Computer Simulation of Target Link Explosion in Laser Programmable Redundancy for Silicon Memory; J Mater. Res.; vol. 1, No. 2; pp. 367-381; March/April, 1986.
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SMH	CG	Harris Sun et al.; "Optimization of Memory Redundancy Laser Link Processing"; SPIE; vol. 2636; June 1995.
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SMH	CJ	General Scanning Inc., "Laser Link Fusing: Guidelines for the Applications of M3XX Laser Systems in Semiconductor Link Fusing Processes," January, 1997
SMH	CK	General Scanning Inc., "Mx Laser Processing Systems Applications Manual," pages 3-1 through 3-6, 1994
SMH	CL	Teradyne, Inc. "Enhancements for Productivity and Simplified Maintenance: Link Sprint™, Sun 3 Workstation, Diode-Pumped Nd:YLF Laser: Three New Options for the M218 Laser Processing System," 1988
SMH	CM	Hecht, "The Laser Guidebook, 2nd Edition," New York, McGraw Hill, 1992, pp. 49, 50, 60-67, 433
SMH	CN	Brannon, James H.; "Excimer-Laser Ablation and Etching"; Circuits and Devices, September 1990

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	AD	5,293,023	03/1994	WANG			
	AE	5,611,946	03/1997	LEONG et al.			
	AF	5,685,995	11/1997	SUN et al.			
	AG	3,740,523	06/1973	COHEN			
	AH	4,399,345	08/1983	LAPMAN et al.			
	AI	4,483,005	11/1984	SMART			
	AJ	5,265,114	11/1993	SUN et al.			
	AK	5,473,624	12/1995	SUN			
	AL	5,569,398	10/1996	SUN			
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	AT	6,337,462	01/2002	Smart			
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	AW	WO 94/06182	03/1994	PCT			
	AX	WO 96/12830	05/1996	PCT			

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	BA	WO 96/15870	05/1996	PCT			Yes No

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	BB		Cook, R.W. et al.; "Connections and Disconnections on Integrated Circuits Using Nanosecond Laser Pulses"; Appl. Phys. Lett., Vol. 26, Number 3; pp. 124-126; February 1, 1975	
	BC		Faber, J., Marshall; "Algorithmic Trimming on Active Circuitry"; Proceedings of the Electronic Components Conference, Washington DC; pp. 248-254, 1974	
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	CG	Harri Sum et al.; "Optimization of Memory Redundancy Laser Link Processing"; SPIE; vol. 2636; June 1995.
	CH	Edward J. Swenson; "Some Present and Future Applications of Laser Processing: An Overview"; Solid State Technology; November, 1983; pages 156-158
	CI	Wells et al.; "A Technique for Automatic Focusing and Alignment of Monolithic Circuits for Laser Trimming"; Proceedings of the Third Annual Microelectronic Measurement Technology Seminar; March 1981.

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